

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): MAKIHARA, et al.
Application No.: 10/594,116
Filed: September 25, 2006
For: METHOD FOR MANUFACTURING A SEMICONDUCTOR INTEGRATED
CIRCUIT DEVICE
Art Unit: 2829
Examiner: Hung, M.
Conf. No.: 7029

AMENDMENT

Commissioner For Patents
P.O. Box 1450
Alexandria, VA 22313-1450

October 14, 2008

Sir:

This is in response to the Office Action mailed June 11, 2008, in connection with the above-identified application. The amendments are listed below and set forth on the following pages.

Amendments to the Claims; and

Remarks are included following the amendments.